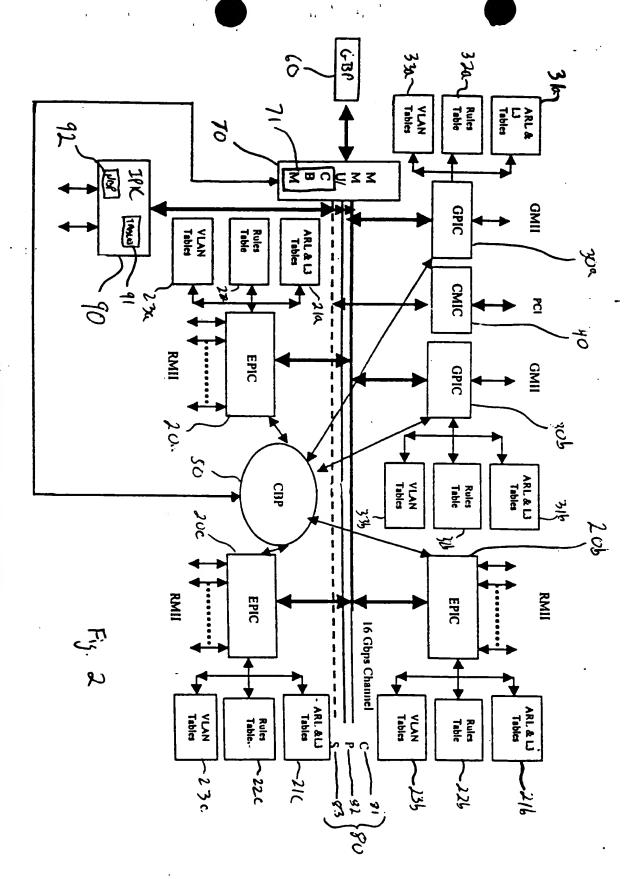
Fig. 1

1.18. 1



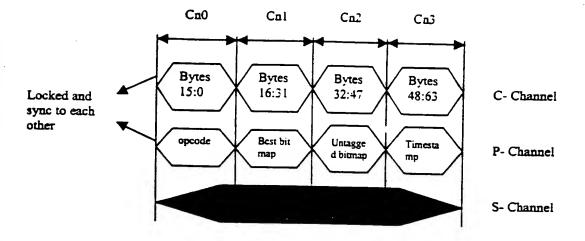
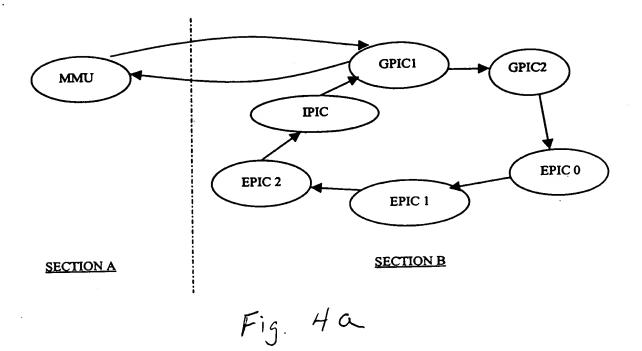


Fig. 3



MMU GPIC1 MMU EPIC 0 MMU EPIC 1

1 Cell\_cycle

MMU EPIC2 MMU IPIC

### **Protocol Channel Messages**

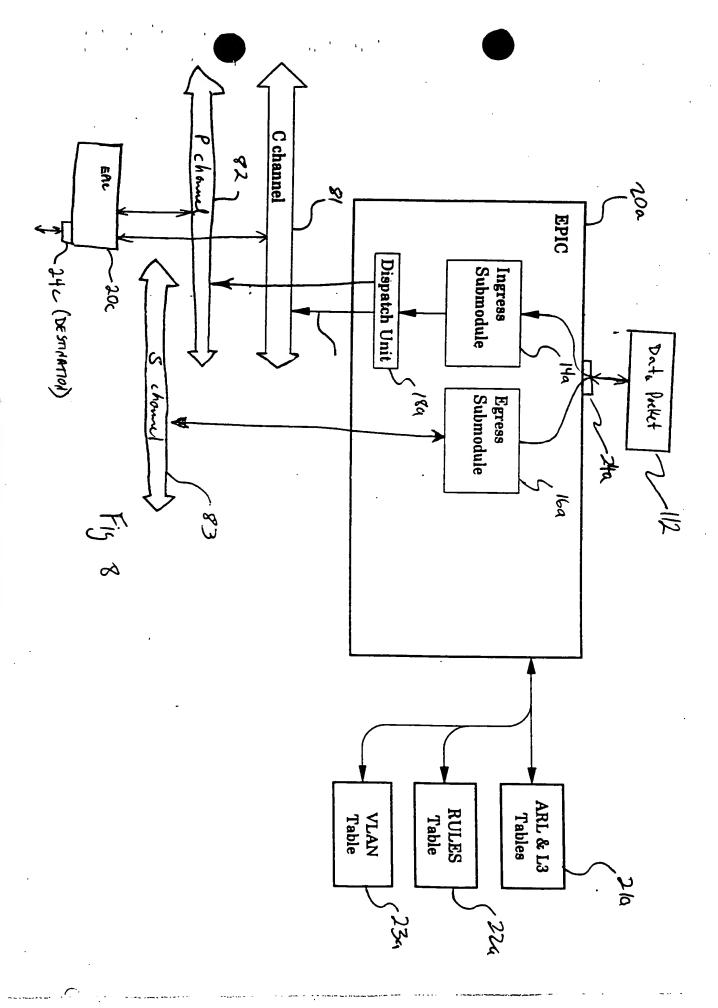
															_
30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
Орс	Ip	Rese	Nxt	Src	Dest 1	Port	Cos	;   J	SE	Cr	P	O		Len	
ode	IPX	rved	cell							C	1	1			
<b></b>		<u> </u>													
62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
"-						Mo	dule I	d Bitr							
L							<u> </u>	<u> </u>	р						
	20	26	24	22	20	18	16	14	12	10	8	6	4	2	0
30	28	20	24	22	20					10		· ·			·
R						В	c/Mc l	01101	шиар						
		<b>,</b>	,	<del>,</del>		1				1 40	1 40	1 20	36	24	32
62	60	58	56	54	52	50	48	46	44	42	40	38		34 Mod	
1	PF New IP checksum								M	MT-M	одта	T	TGID	opco	
M_	1								LL			1		opco	ue
		<del>,</del>	,	,						10		1 6	1 4	<u> </u>	0
30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	<u> </u>
U				Untag	ged F	ortbit	map /	Src P	ort Ni	ımber	(bit0	<u>১)                                   </u>			
62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	32
	svd	Mat	ched			Vla	n Id			5	Src Po	ort	Re	mote l	Port
1	,,,		lter												
		1 11.	1101	l											
30	20	126	24	22	20	18	16	14	12	10	8	6	4	2	0
30	28	26		U Opco		10	110	17_	<del>  **</del> -	1.0			amp		` -
<u> </u>				O Oper	- ACS				L		<u> </u>	1103	шпр		
					T		Т		1 44	1 40	1 46	T 20	26	34	32
62	60	58	56	54	52	50	48	46	44	42	40	38	36	34	1 32
R	L3 Port Bitmap														

### Side Band Channel Messages

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
	Opcod	le		est Po estinat Dev I	tion		Src P	ort		Data]	Len	E	EC ode	Cos	С
				· · · · · ·			Ad	dress							
							Γ	ata							

Loyer Seven- Application
Loyer Six Presentation
Layer five- Scssion
Layer four- Transport
Layer three- Network
Layer two- Duta link
Layer one- Physical

Figure 7 Prior Art



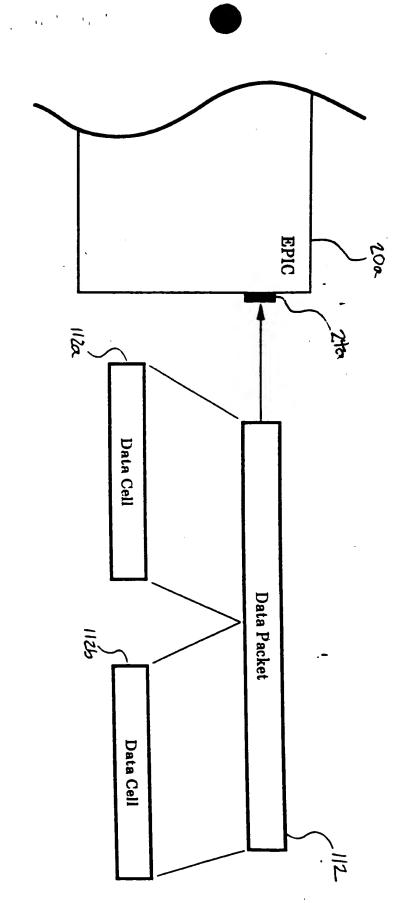
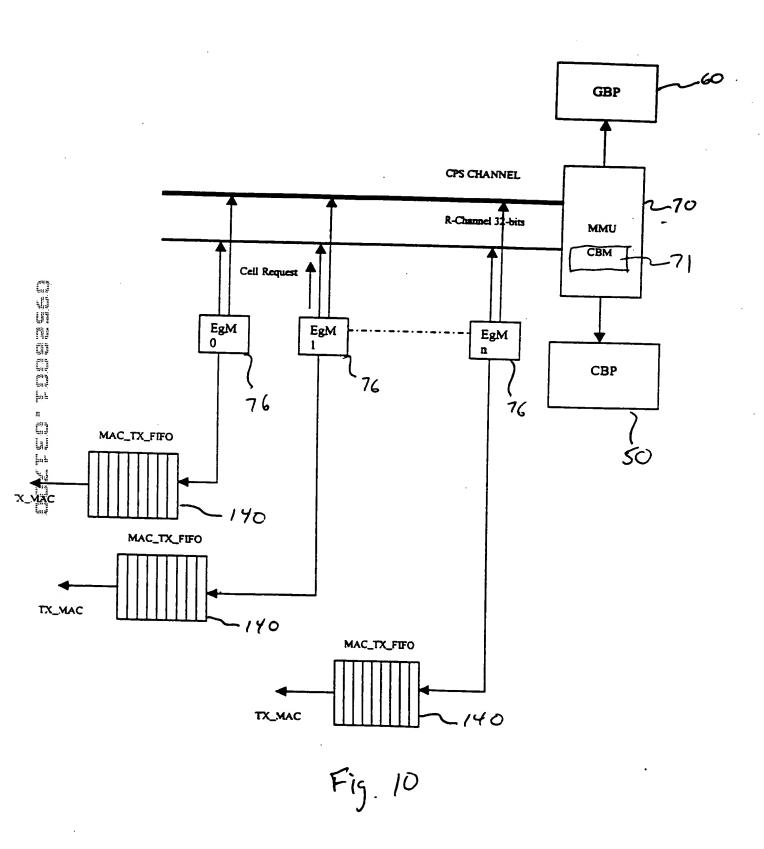


Fig. 7



Line 0 FC   LC   BC/MC   Cpy_cnt(5b)   Cell_length (7b)   CRC (2b)   NC_header (16b)   Src Count(6   Time_Stamp (14b)   O bits(2b)   P  NextCellLen(2b) CpuOpcode(4b)  Cell_data (0-9B)										
Line 2	Cell_data (10-27) Bytes									
Line 2	Cell_data (28-45) Bytes									
	Cell_data (46-63) Bytes									

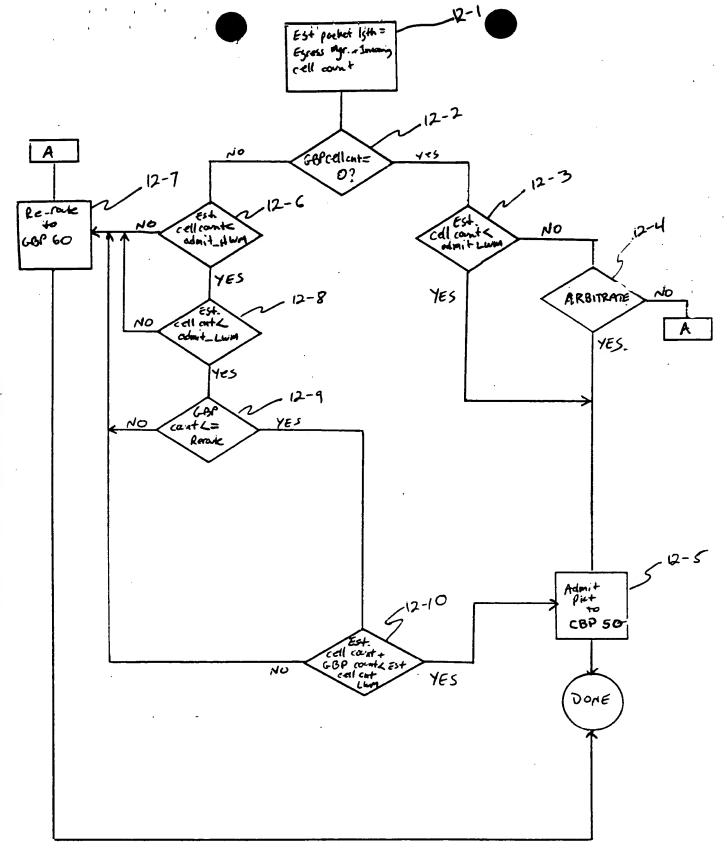


Fig. 12

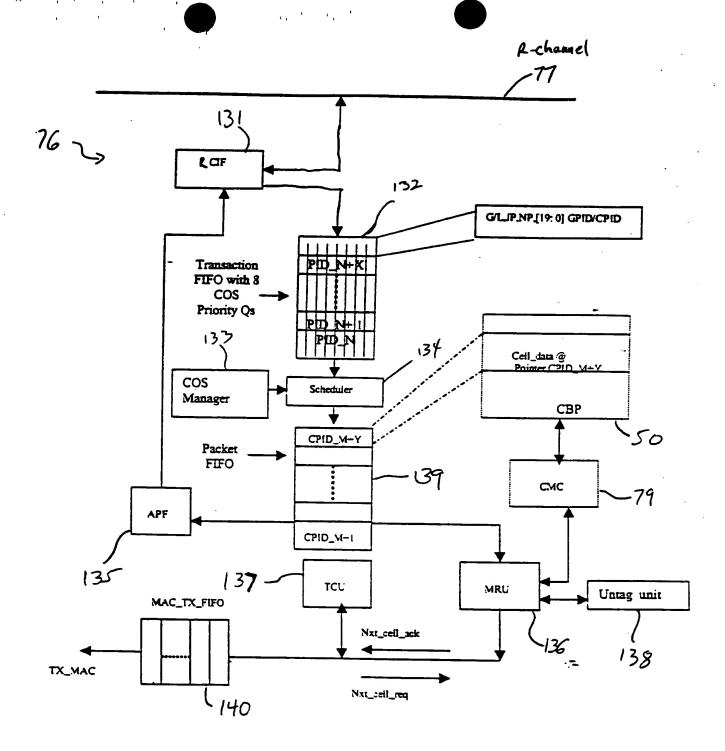
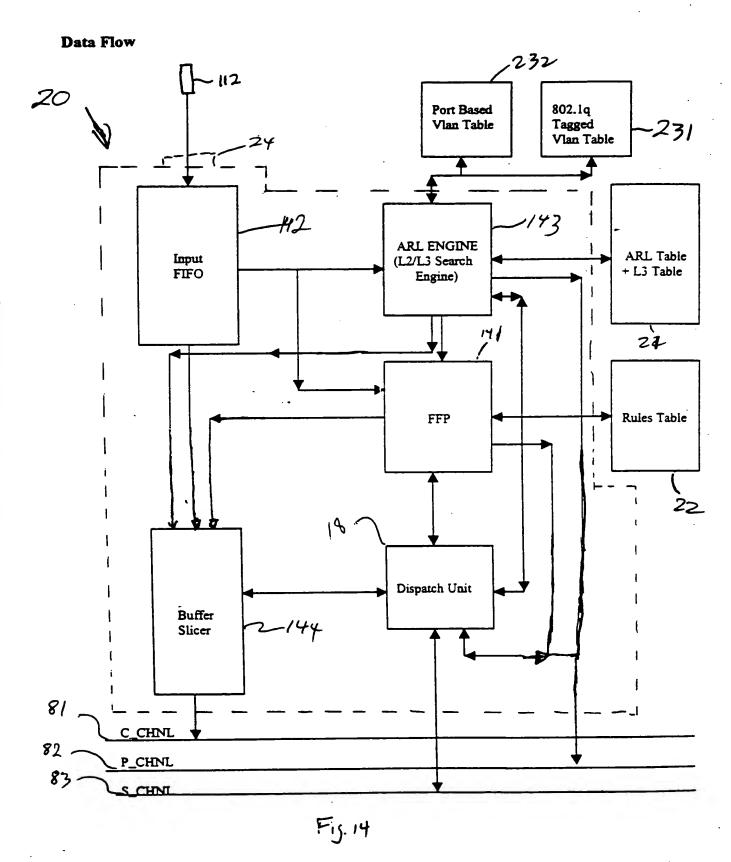
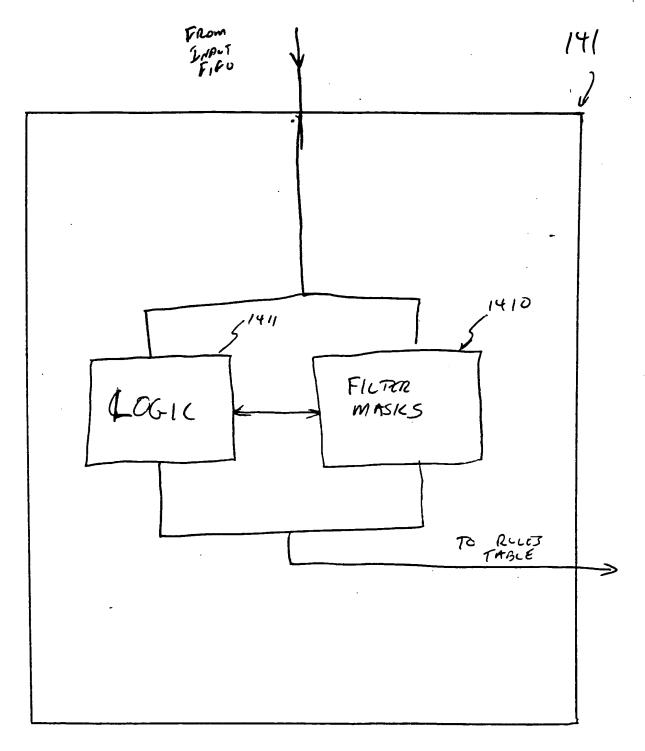


Fig 13

der o

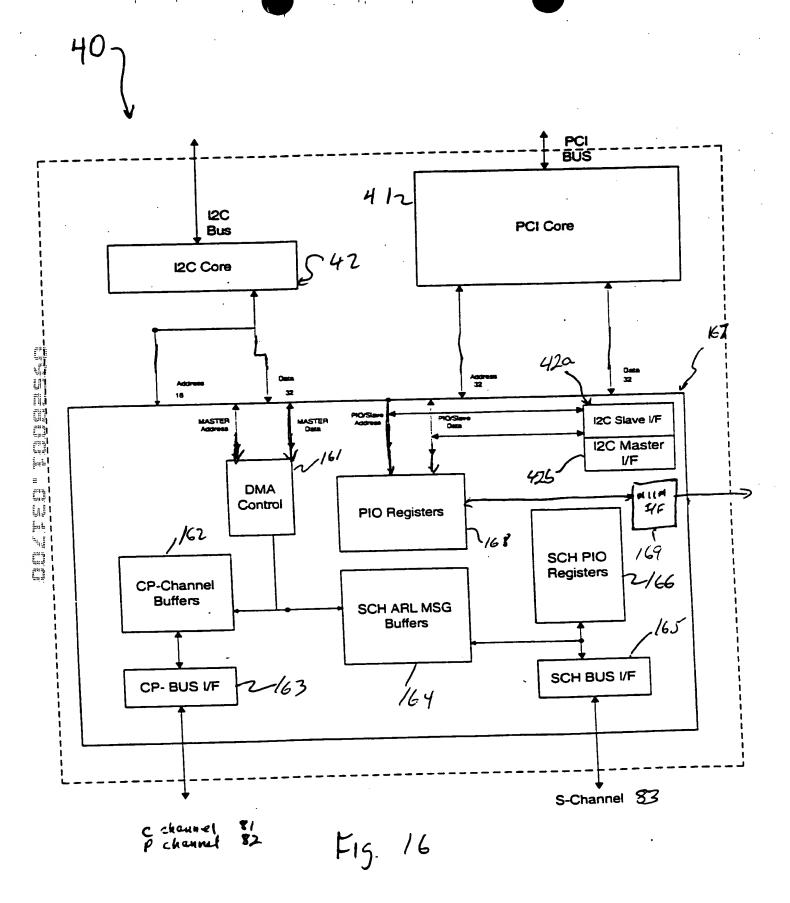


87.10



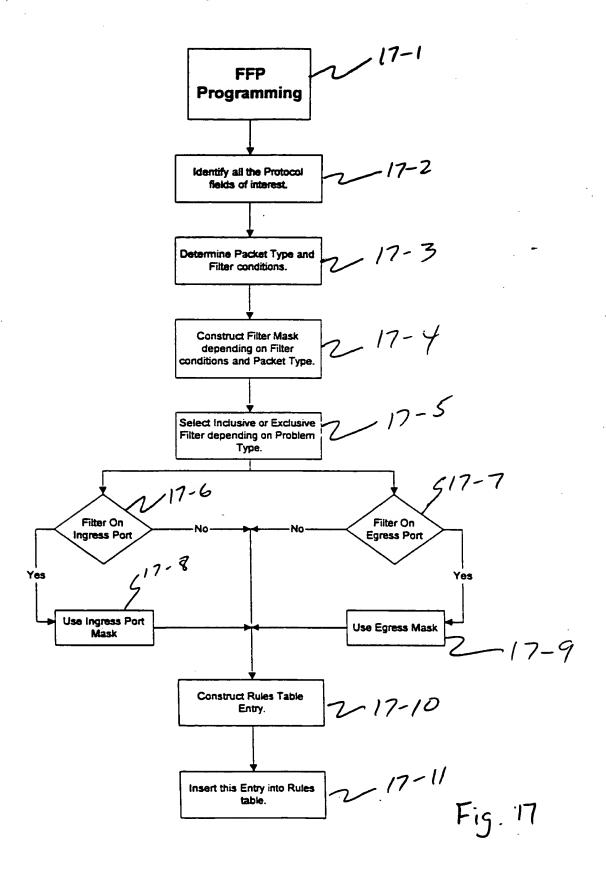
F16. 15

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# FFP Programming Flow Chart



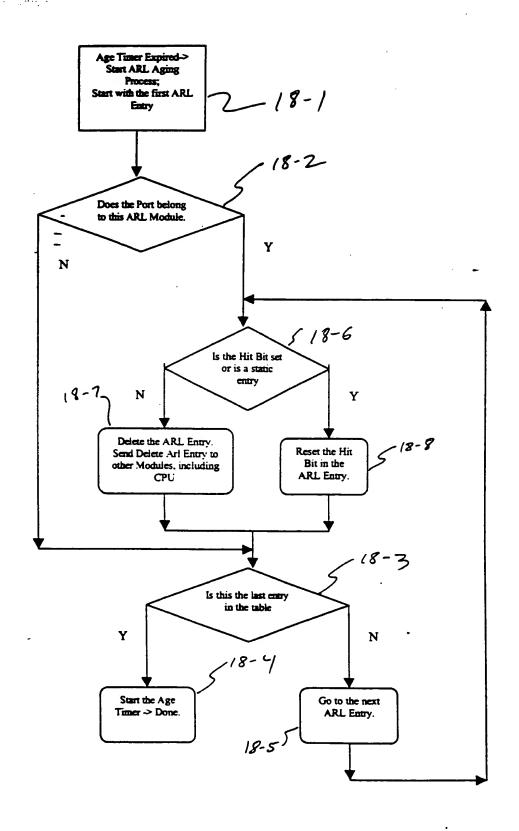


Fig. 18

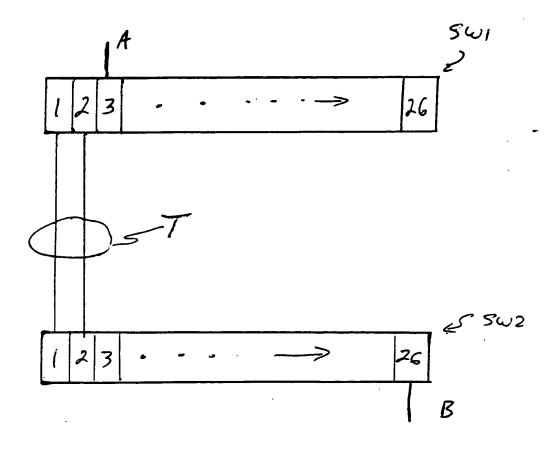


Fig. 19

Field .	Header	Size	Offset For Ethernet II Untagged	Offset For Ethernet II Tagged	Offset For SNAP Untagged	Offset For SNAP Tagged
Destination Mac Address	Mac	6 Bytes	0	0	0	0 .
Source Mac Address	Mac	6 Bytes	6	6	6	6
Protocol Type	Mac	2 Bytes	12	16	20	24
Destination SAP	802.3	1 Byte	NA	NA	14	18
Source SAP	802.3	1 Byte	NA	NA	15	19
802.1p Priority	Mac	3 bits	NA	14	NA	14
VLAN Id	Mac	12 bits	NA	14+ 4b	NA	14+4b
TOS Precedence	IP	3 bits	15	19	23	27
Differentiated Services	IP	6 bits	15	19	23	27
Source IP Address	IP	4 Bytes	26	30	34 .	38
Destination IP Address	IP	4 Bytes	30	34	38	42
Protocol	ĪP	1 Byte	23	27	31	35
Source Port	TCP/ UDP	2 Bytes	34	38	42	46
Destination Port	TCP/ UDP	2 Bytes	36	40	44	48
TCP Control Flags (For aligning on Byte boundary 2 bits of reserved bits preceding this field is included)	ТСР	1 Byte	47	51	55	59
Data at Offset 1	NA	8 Bytes	Data	Data	Data	Data
			Offset1	Offset1	Offset1	Offset1
			From	From	From	From
			start of	start of	start of	start of
1			IP / IPX	IP/IPX	IP / IPX	IP/IPX
			Header_	Header	Header	<u>Header</u>
Data at Offset 2	NA	8 Bytes	Data	Data	Data	Data
.1		l j	Offset2	Offset2	Offset2	Offset2
		į	From	From	From	From
			start of	start of	start of	start of IP / IPX
1			IP/IPX	IP /IPX	IP / IPX	1
			Header	Header	Header	Header
Data at Offset 3	NA	8 Bytes	Data	Data	Data Offset3	Data Offset3
			Offset3	Offset3	Offset3 From	From
1	1		From	From	start of	start of
1			start of	start of IP / IPX	IP / IPX	IP / IPX
			IP / IPX	Header	Header	Header
	+	10 D- 1-	Header	Data	Data	Data
Data at Offset 4	NA	8 Bytes	Data	Offset4	Offset4	Offset4
			Offset4	From	From	From
			From start of	start of	start of	start of
			IP /IPX	IP / IPX	IP/IPX	IP / IPX
		-	Header	Header	Header	Header

FIGURE 20

### Filter Mask Format:

Filter Enable (1b)	Counter (5b)	Rem Port (1b)	Output Mod (5b)	Output Port (6b)	TOS I			f Serv 6b)	802	2.1p Prior (3b)		
NMA Enb (1b)	No Match Action (10b)		Data Offset	Data Offset 2 (7b)	Data Offset 1 (7b)	set Port		Egre Mod Mas (5b	Id k	Egress Port Mask (6b)		
	Field Mask											

## Field Mask Format:

Dest Mac	Src Mac	Prot type	Dest SAP	Src SAP	802.1 p	Vlan Id	TOS Prec	Diff Serv	Src IP	Dest IP	Prot IP-	Src Port	Dest Port (2B)	
addr (6 B)	addr (6 B)	(2 B)	(1 B)	(1 B)	Prio (3 b)	(12b )	(3b)	(6b)	addr (4B)	addr (4 B)	(1B)	(2B)	(2B)	

TCP Cntr Flags	Data 1	Data 2	Data 3	Data 4
(1B)	(8B)	(8 B)	(8B)	(8B)

Appless (explution parsing pocket to entrock selected gelds Construct field value Go through all filters + opply mosk Concatenate musk results with filter number-generate search Key search rules table for south key match perform action as specified bound on match

And the first term of the state of the state

12

Count er (5b)	Output Mod (5b)	Output Port (6b)	TOS_P (3b)	Diff Services (6b)	802.1p Priority (3b)	Actio ns (11b)	Filter Select (3b)	Ingres s Port (6b)	Egrs Mod (5b)	Egrs Port (6b)	Filter Value (512 b)
	ļ										
	<del> </del>		<u> </u>		·						
	+										
	<del></del>		<u> </u>								
Ĺ	<u> </u>										

Fy. 23

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
						Sou	ırce II	Add	ress						
						Mul	ticast ]	P Ad	dress						
r	r L3 Port Bitmap														
						L3	Modu	le Bit	map						
				Uı	nused						TTI	,	So	urce ]	Port
										1	hresh	old			

Fig. 24

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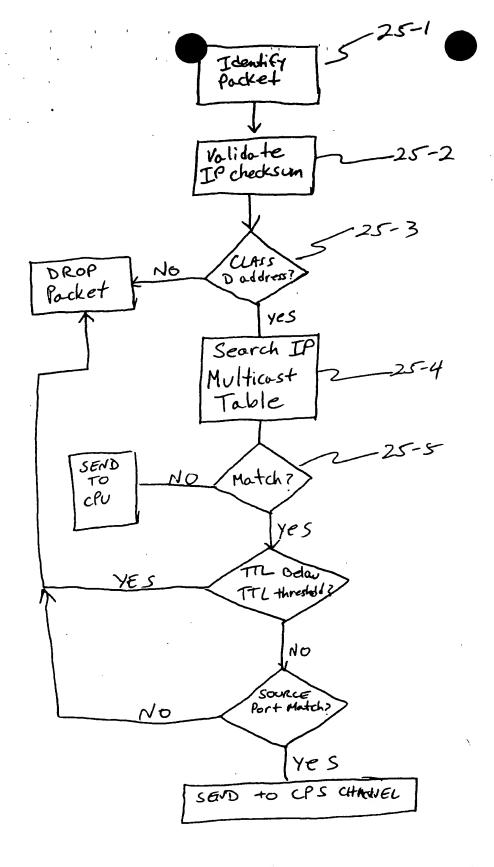
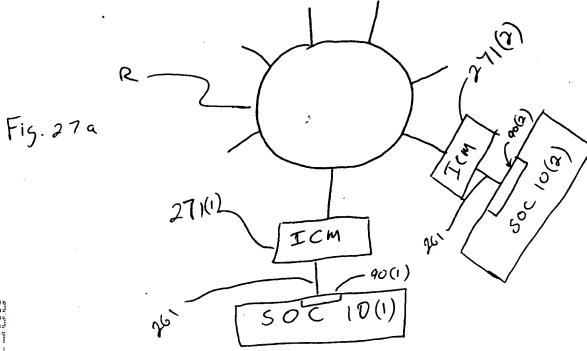


Fig. 25



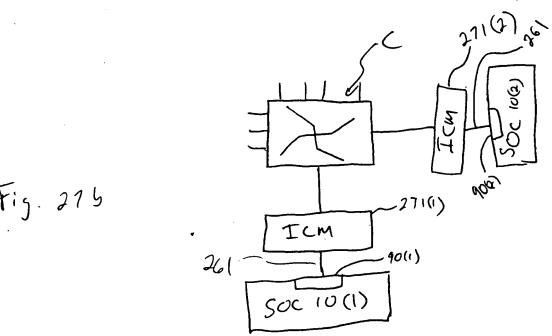


Fig. 215

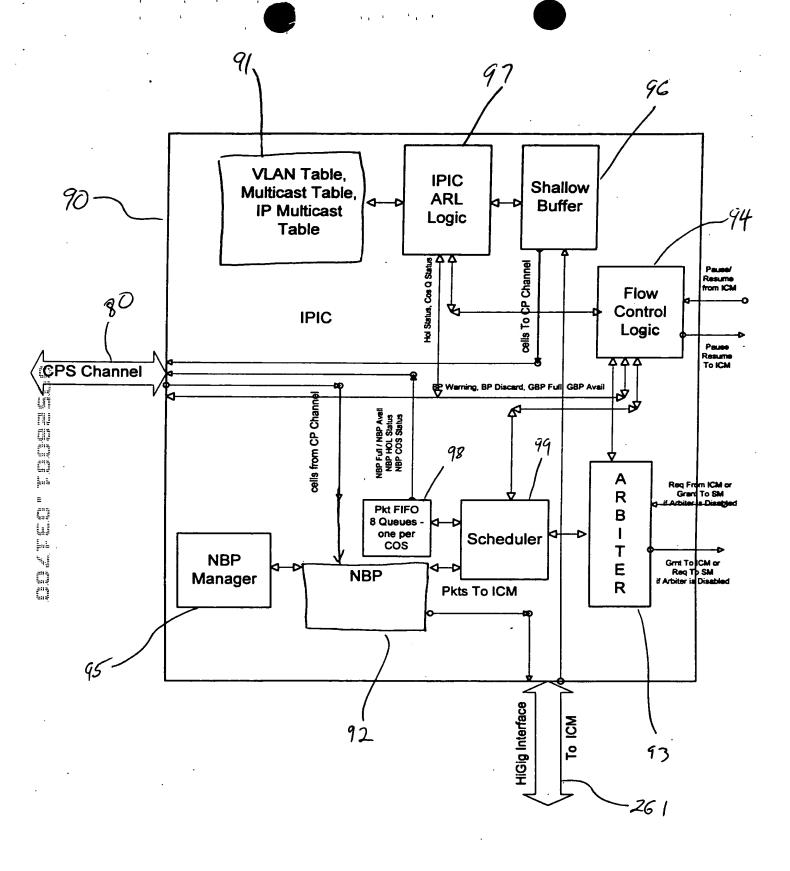
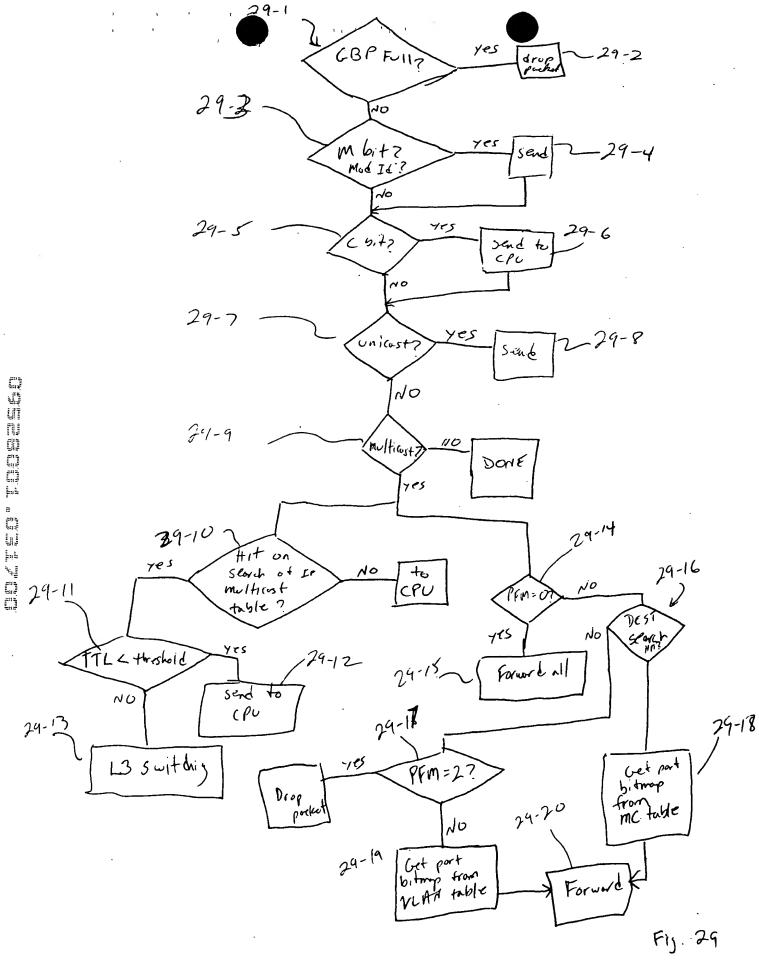


Fig 28.



COS	С	NCA	802.1p	Rate	Rate	Rate	New	New	New
Queue	P	(2b)	Priority	Counter	Counter	Discard	Code	COS	802.1
(3b)	F		(3b)	(8b)	Threshold	Thresho	Point	Queue	Priority
			,		(8b)	ld (8b)	(6b)	(3b)	(3b)

FIGURE 30

Offset Field	Offset 1	Offset 2	Offset 3	Offset 4
000	0-15	16-31	32-47	48-63
001	8-23	24-39	40-55	56-71
010	16-31	32-47	48-63	64-79
011	24-39	40-55	56-71	72-87
100	32-47	48-63	64-79	80-95
101	40-55	56-71	72-87	88-103
110	48-63	64-79	80-95	96-111
111	56-71	72-87	88-103	104-119

Figure 31

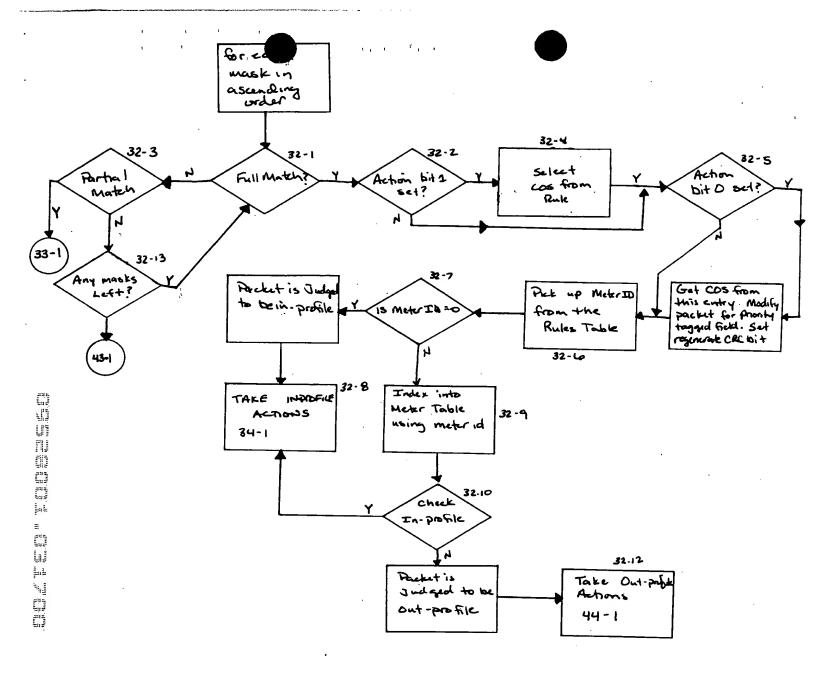
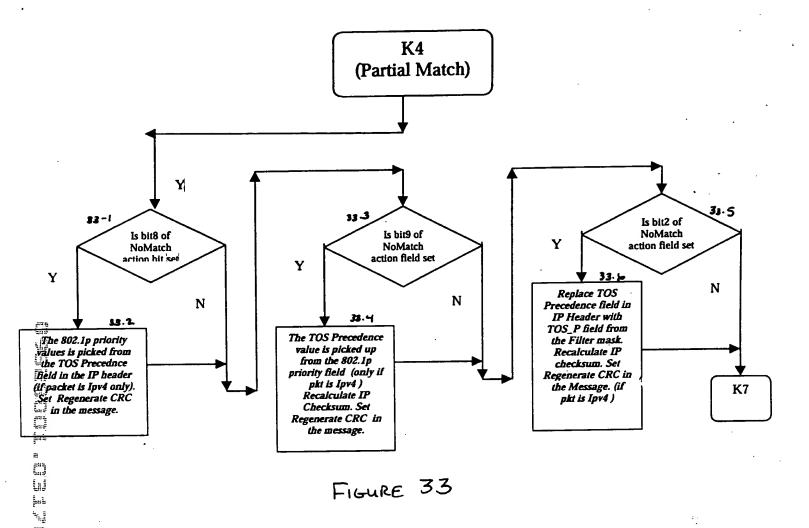
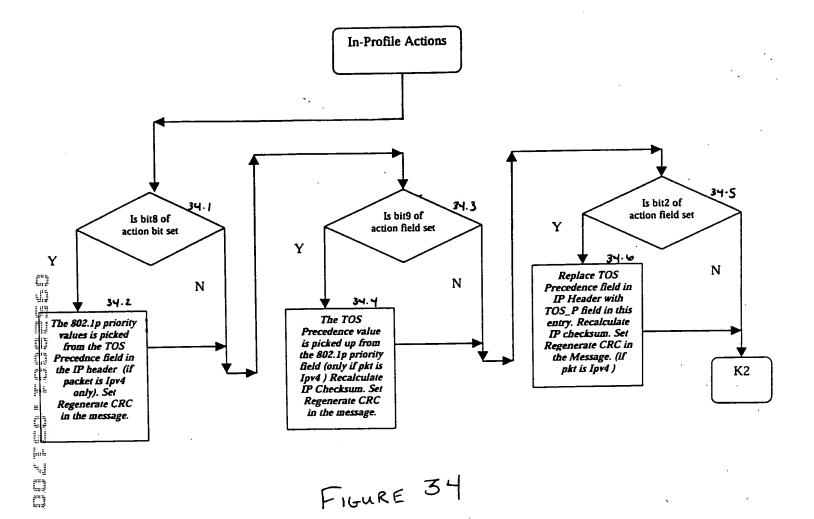
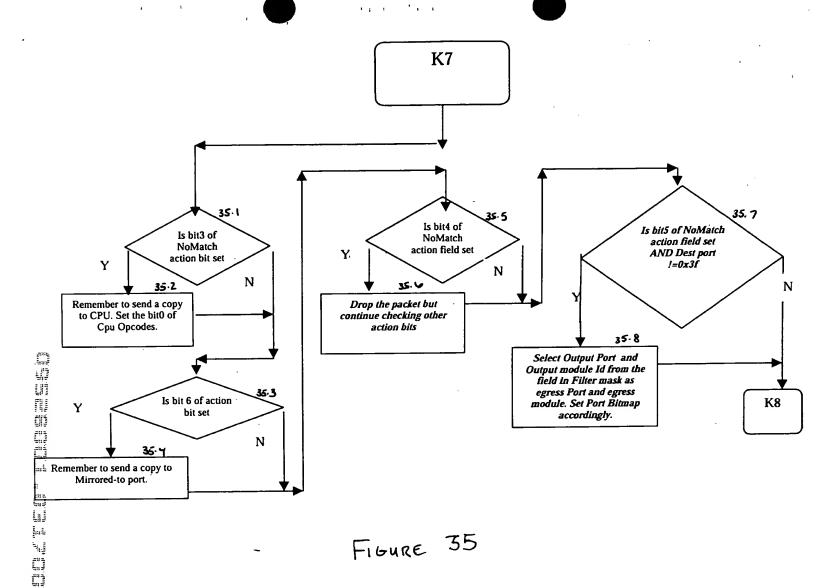


FIGURE 32







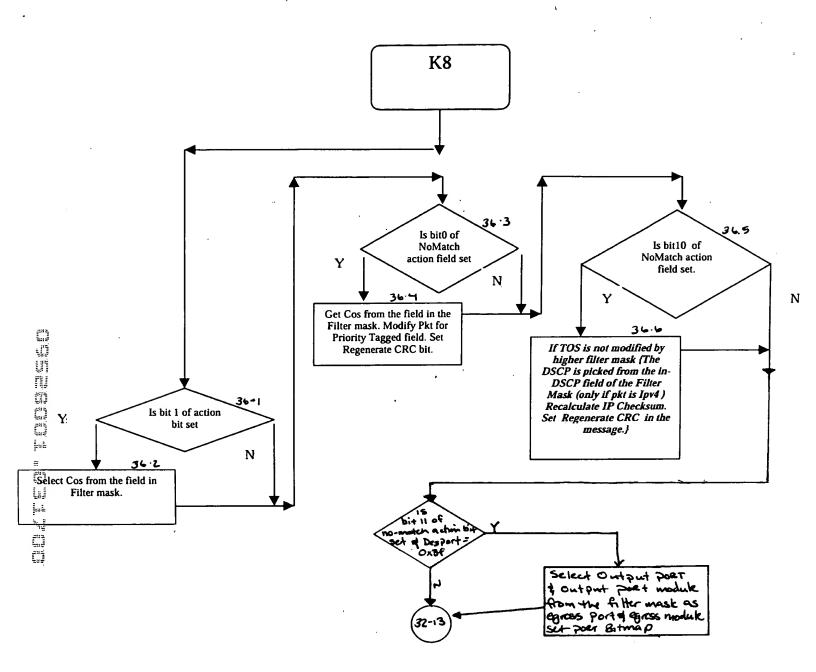
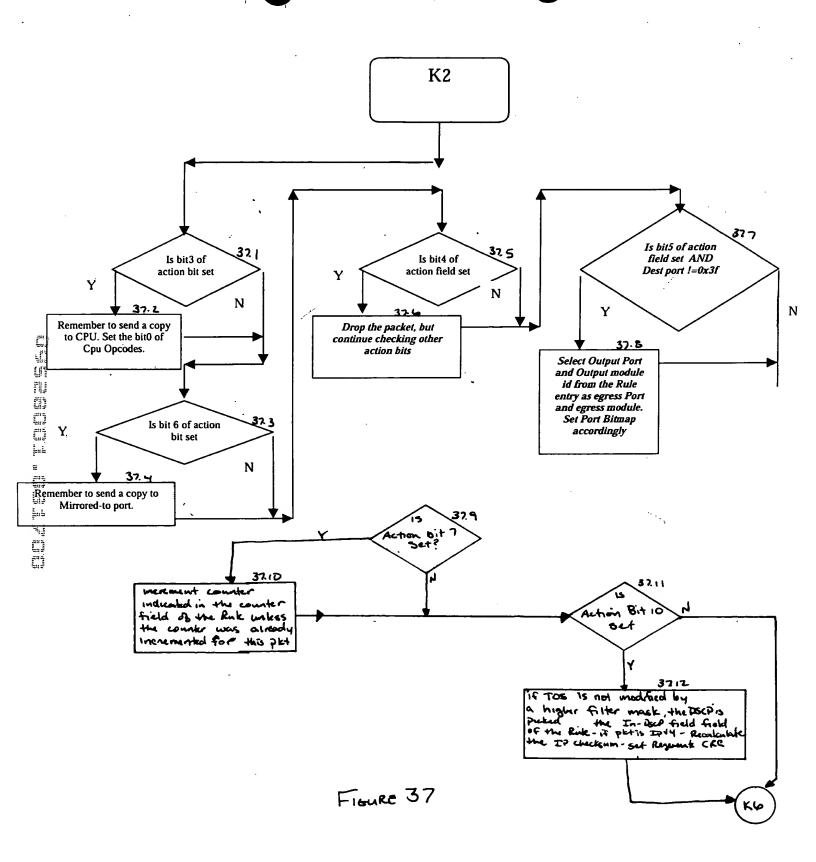
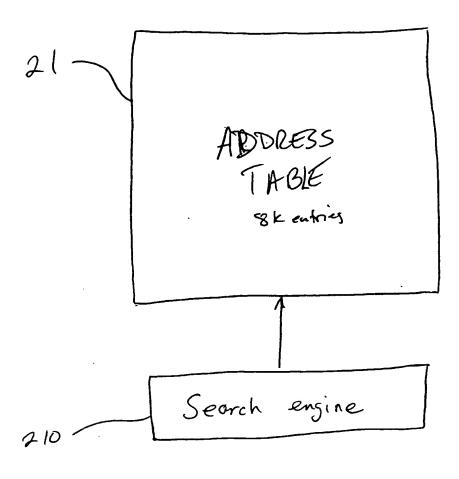


FIGURE 36

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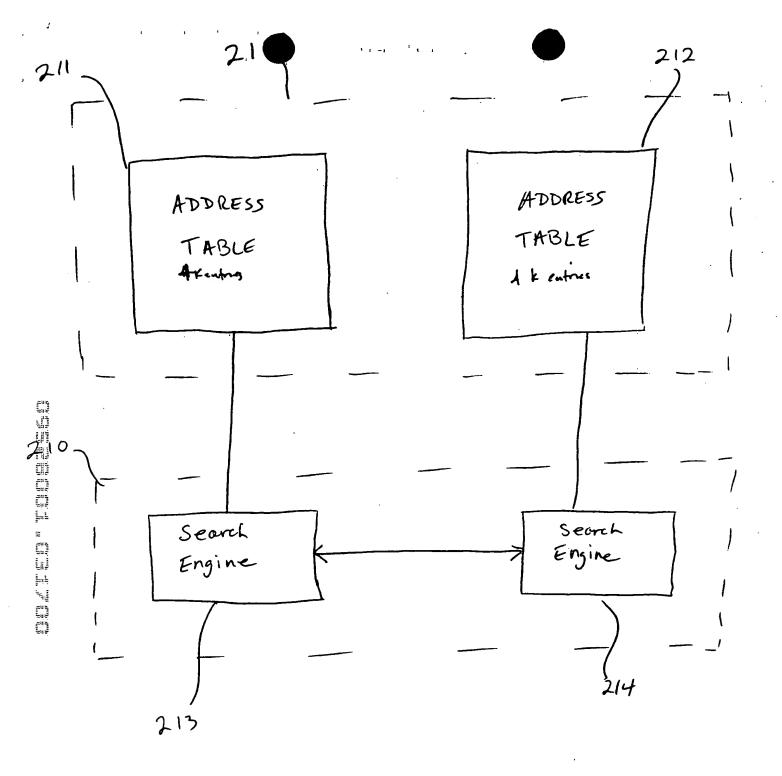


Fig. 39

Figure	40a

ŀ	address	entry
	31	
<b>1</b> -	30	ĀF
217	29	AD
	28	AC
	28 27	AB
	26	AA
	25	Z
•	24	Y
	23	×
	22	w
	21	V
	20	U
	24 23 22 21 20 19	T
	18	S
	17 16	R
	16	Q
	15	P
	14	0
	13	N
	12	M
	111	늬
	10	``
	9	ار ۱
	7	
	,	G
	5	l Ĕ
	4	E
	3	اً ا
	2	c
	1	В
	13 12 11 10 9 8 7 6 5 4 3 2	A A A A A A X X X X X Y Y R R R R R R R R R R R R R

<b>-</b>	·		-11		212 C-	· - (
١,	address	entry		address	entry	1
	30 28 26 24 22 20 18 16 14 12 10 8	# 6 4 × 5 0 8 8 0 8 × -		31 29 27 25 23 21 19 17 15 13 11 9	AF AD AB Z X V T R P N L J H F	111111
	4 2 0	G E C A	·	5 3 1	D B	
(			— Fig 40	06	- 21	

Figure 4/a

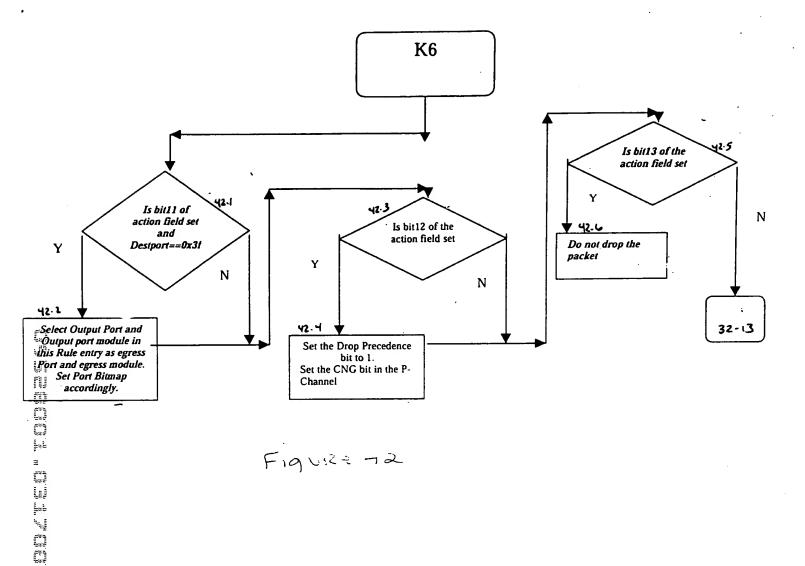
	address	entry
	31	NN
	30	MM
21	20	MM LL
<b>≠</b> 1	29 28	KK
	27	
	26	KK JJ GH
•	25	CF
	24	CC
	23	BE
	23 22	BD
	21	ВС
	20	BC BA
	19	AC
	18	AB
	18 17	44
	16	Y
	15 14 13 12 11 10	×
	14	
	13	
	12	s s
	11	R
	10	
	9	N
	8	M M
	$\mathbf{I}$	
	1	i K
	1	
	1	
	8 8 7 6 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	ACBAYXVTSRQNMLKJGEDCB
	'	<u> </u>

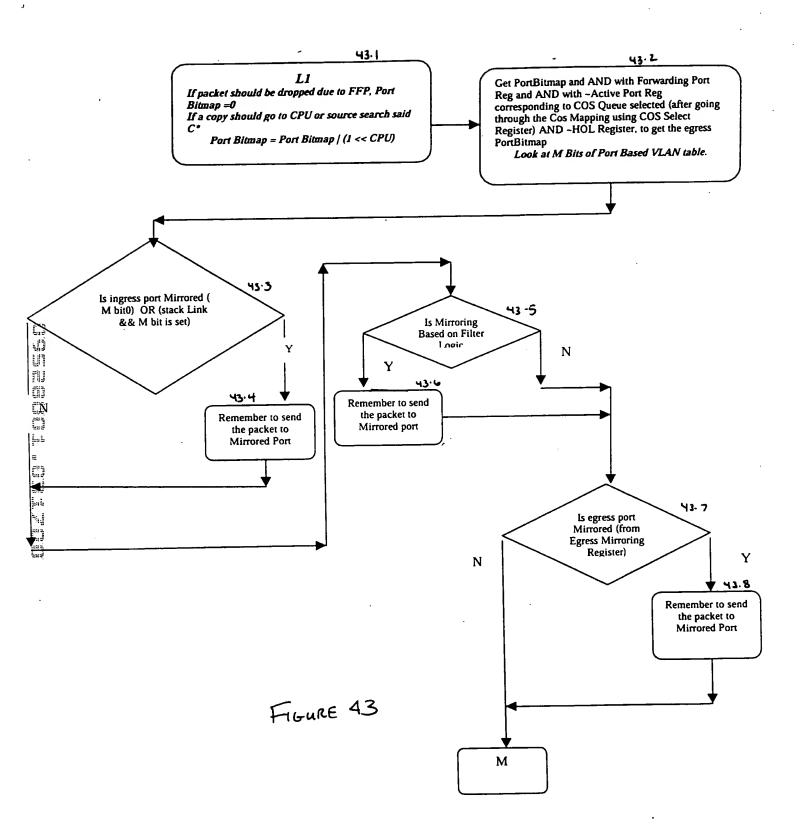
ſ	address	entry
	30	MM
-	28	KK
	26	GH
	24	CC
1	22	BD
	26 24 22 20 18	GH CC BD BA AB
1	18	
	16	Y
	14	V
	16 14 12 10	S
	10	Q
	8	M
	6	K
	8 6 4 2 0	YVSQMKGDB
1	2	D
	0	В

	/
address	entry
31	NN
29	LL
27	JJ
25	CF
29 27 25 23 21 19	BE
21	BC
19	AC
· 17	AA
17 15 13	×
13	T
11	R
. 9	N
7	L)
9 7 5 3	OBCLZZHXXXXA
3	
1	C

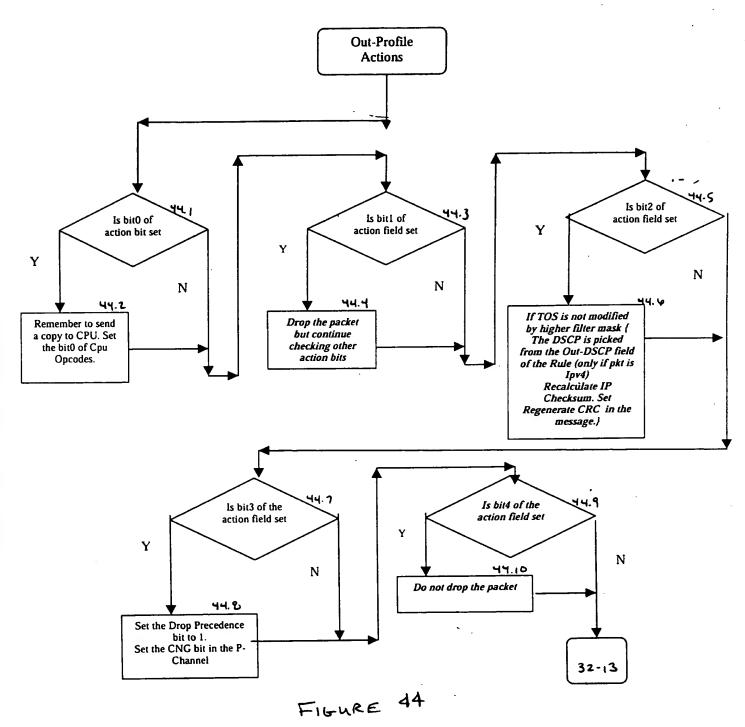
Fig 414

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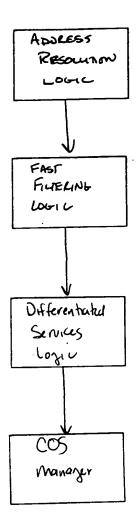


FIGURE 45

40.



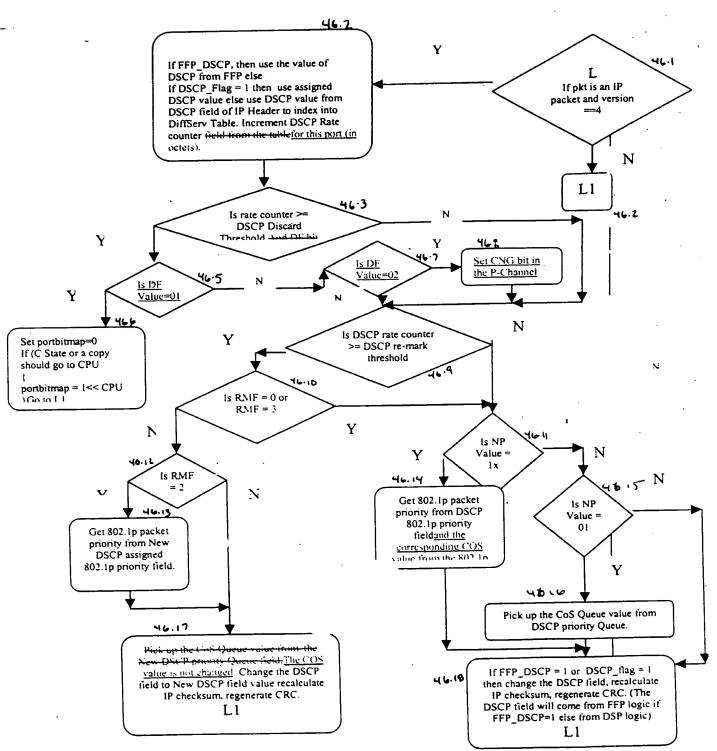


FIGURE 46

